

AN IMPLEMENTATION OF FOURIER TRANSFORMS BASED ON DISTANCE RELAYING ALGORITHM USING EMTP MODELS

C. H. Kim, M. H. Lee, Y. C. Shin, J. B. Chae, S. W. Park, S. H. Byun

School of Electrical and Computer Engineering
Sung Kyun Kwan University
Suwon, KOREA

Abstract - This paper presents a new implementation of EMTP MODELS for distance relay modeling, in which we simplified the procedures of system modeling and distance relaying system by using a single structure of MODELS.

In this paper, we tried to integrate the modeling of the power system and the protective system in one program module. The purpose of this paper is to provide an systematic relaying concepts by modeling digital relaying system using MODELS functions within EMTP in a closed-loop manner. Various elements of digital protective relaying are organized to generate an systematic approach to modeling the actual hardware of digital relaying systems. Single phase ground fault is simulated and various distances and phases are considered.

Keywords - EMTP, MODELS, Digital Relaying, Fourier Transform, DC-offset, Fault Location Estimation

I. INTRODUCTION

The protective relaying system is required to detect the abnormal signals indicating the faults in transmission system, separate the faulty part of the system from the normal operation portion and hence prevent the propagation of the fault sources. The electrical energy transmission system becomes complicated, and longer in distance as power plants tend to be larger and located in a small bound due to environmental restrictions. Digital protective relaying system as a consequence for such a large system control comes to play a role instead of the traditional analog relaying system which is known for a large variance in distance estimation. It has to, therefore, estimate the region of the fault source and block the propagating effect as fast as possible[1-5].

In order to develop a new relay system to be used it needs to be tested in a miniature system or to be simulated with the faulty data obtained usually from

EMTP. With a simulation method, ones actually need the two steps: obtain the simulation data for various fault cases and simulate the algorithm with the data. Combining the two separated tasks into one programming module in simulation is very much called for. Chaudhary and Phadke[6-8] developed the EMTP EPRI/DCG Version 3.0 that allows all the possible FORTRAN subroutine to be used. The MODELS from EMTP recently introduced, make it possible to control the inter-operation between the power system and protective system and to model system operations[9-12]. In this paper, we tried to integrate the modeling of the power system and the protective system in one program module. All the procedures for the simulation of a digital relaying system are in a EMTP formatted file, thus simplifying the evaluation of a new protective algorithm.

II. EMTP MODELS

MODELS is a symbolic language interpreter for the EMTP which is recently popularized for electromagnetic transient phenomenon modeling. MODELS provides the monitoring and controllability of power systems as well as some other algebraic and relational operations for programming. MODELS approaches to model the power system by describing the physical constants and/or the subsystems functionally for target systems. With some compromised functions it is also called a new TACS.

III. IMPLEMENTATION OF DISTANCE RELAYING BY MODELS

Distance relaying system implemented in a microprocessor is widely used for protecting transmission system which requires reliability in maintenance. Its fast operation and independence of the capacity of the power system are responsible for the popularity in use.

Distance relaying scheme makes use of the transient voltage and current values passed through the CT and PT for the calculation of the impedance. The distance relaying methods rely on its estimation of fault distance upon the partial values and the convergence of them, which produced much work of research in this field. Among others, the transmission line protection based on the fundamental frequency signals is widely used. We studied a new implementation for it with the point of view of fast and exact extraction of the fundamental component.

When a fault happens the transient voltage and current values are mainly composed of the high frequency and exponentially decreasing dc-offset components. For the reliable estimation of the fault distance the fundamental component needs to be extracted via various digital signal processing algorithms. In this paper we implemented the anti-aliasing low-pass filters and the dc-offset removal filter with MODELS.

A. Anti-aliasing Low-Pass Filter

In order to meet the sampling theory the sampling rate should be as two times large as the maximum frequency in the analog signal. Sometimes sampling with smaller sampling rate results the aliasing effects to the discrete time signals. The anti-aliasing filter is used to remove such aliasing effect as well as the high frequency components. For the purpose of removing the unwanted components the analog second order butterworth low-pass filter is used[14]. The specifications for the filtering is that the passband cutoff frequency is 60Hz, the stop-band cutoff frequency 360Hz, stop-band attenuation 28dB and the sampling frequency 1.8kHz (300 samples/cycle).

The transfer function of the low-pass filter

$$H(s) = \frac{1}{s^2 + \sqrt{2}s + 1} \quad (3-1)$$

is transform by the bilinear transformation, resulting the digital transfer function of

$$H(z) = \frac{a_0 + a_1Z^{-1} + a_2Z^{-2}}{1 + b_1Z^{-1} + b_2Z^{-2}} \quad (3-2)$$

$$\begin{aligned} \text{where, } a_0 &= 0.000108058, & a_1 &= 0.000216116 \\ a_2 &= 0.000108058, & b_1 &= -1.9708328899 \\ b_2 &= 0.970815132. \end{aligned}$$

B. DC-offset Removal Filter

When the incidence breaks the abnormal components of the voltage and the current are due to the high component frequencies and the dc-offset component.

The mentioned anti-aliasing filtering removes the most of the high frequency components but not the dc-offset

effect. The next step is to apply the dc-offset removal filter for the removal. We assume that the dc-offset component is of exponential form when a fault occurs:

$$x_k = \sum_{n=1}^{\infty} X_n \sin\left(\frac{2\pi nk}{N}\right) + A \exp\left(\frac{-k\Delta t}{\tau}\right) \quad (3-3)$$

where, Δt is sampling interval,
 τ is time constant,
 N is samples per period.

$$y_k = x_k - x_{k-1}/\exp(\Delta t/\tau) \quad (3-4)$$

where, Δt is sampling interval,
 τ is time constant.

Applying (3-3) to (3-4) yields the following signal after the removal

$$y_k = \sum_{n=1}^{\infty} X_n a_n \sin\left(\frac{2\pi nk}{N} + \varphi_n\right) \quad (3-5)$$

where, $a_n = \sqrt{(E_n^2 + F_n^2)}$, $\varphi_n = \tan^{-1}(F_n/E_n)$,
 $E_n = 1 - [1/\exp(\Delta t/\tau)]\cos(2n\pi/N)$,
 $F_n = [1/\exp(\Delta t/\tau)]\sin(2n\pi/N)$.

As can be seen (3-5) the filtering (3-4) removes the dc-offset component.

C. Digital Filter for Fundamental Frequency Component Extraction

There are three approaches used in digital transmission line protection algorithms. These approaches depend on the form of the final input signal used to make the relaying decision.

We, in this paper, used the widely accepted method which uses the fundamental frequency signal. The voltage and the current values from the extracted fundamental signals are used to calculate the impedance from the system to the point where the fault occur. There have been proposed many algorithms for the fundamental component extraction[15-16]. Fourier transform, Walsh function, Harr transform and block pulse functions are the orthogonal functions that are used for this purpose.

We used the block pulse function methods and implemented with MODELS structure.

D. Fundamental Component Signal Extraction via DFT

Assume that the N samples are obtained for each period and call the discrete time signals $x(k)$. Then the DFT of the sampled signal

$$X(n) = \sum_{k=0}^{N-1} x(k) W_N^{nk} \quad (n=0, 1, 2, \dots, N-1) \quad (3-6)$$

where, $W_N^{nk} = e^{-j(\frac{2\pi nk}{N})}$

The n is the order of the harmonics. The fundamental frequency signal are the one with n = 1. In (3-6), all the N samples were used in the calculation of the fundamental frequency signal, resulting full cycle DFT (FCDF), but for faster calculation half cycle DFT (HCDFT) is sometime used.

IV. SIMULATION RESULTS AND DISCUSSION

A. Simulation Method

A set of simulation experiments were carried in order to show the validity and the simplicity of the EMTP MODELS implementation of the power system and the protective relaying system. The subsystems are largely obtaining the fault data, anti-aliasing low-pass filtering, removing of dc-offset component, and extraction of the fundamental component signal and the impedance calculation. All the subsystems are in a single EMTP file and the different experiments are carried for various fault types and fault distances.

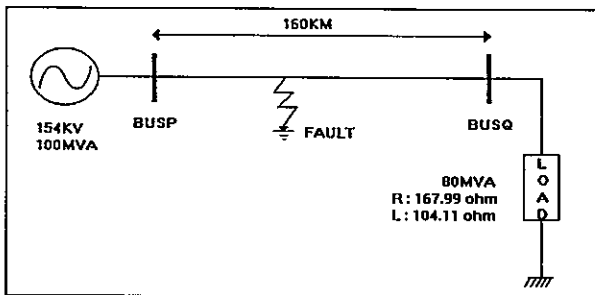


Fig. 1. Model system.

The transmission system is modeled as the total length of 160Km and of 154Kv as shown in Fig.1.

The size of conductor is ACSR 477MCM 240 and the electrical constants are shown in Table 1.

Table 1. Data of line and source.

Line constants	Z_0			
	R [Ω /km]	0.3434	0.1342	0.1342
	L [Ω /km]	1.3158	0.4765	0.4765
	C [μ F/km]	0.0052	0.0090	0.0090
Source	Capacity	100 MVA		
	Power factor	0.85		
	Subtransient reactance	7.71 %		
Load capacity	80 MVA			

The simulation assumed a single line-to-ground fault which is the most often the case. A case with A-phase to ground fault is carried out for various distance and phase angles as in Table 2.

Table 2. The simulated fault type and fault condition.

Fault type	ILG: A-G	Fault distance	Voltage degree	
			10% (16km)	0°
			20% (32km)	
			30% (48km)	
			40% (64km)	30°
			50% (80km)	
			60% (96km)	
			70% (112km)	60°
			80% (128km)	
90% (144km)				
			90°	

B. Modeling Anti-aliasing Low-pass Filter with MODELS

We examined the validity of the MODELS-implemented anti-aliasing low-pass filter. It is known that the high frequency components are found more at around the 90-degree than at around the zero-degree. For the validity of the filtering we modeled the fault data at the 90-degree for the case of A-phase to ground fault and fault distance of 50% from the system.

Fig.2 and Fig.3 show the voltage waveforms before and after the anti-aliasing low-pass filtering. The outputs of the filter shown in the Fig. 3 reveals the filter effect by removing the high frequency components. Also the phase delay and magnitude attenuation can be observed from the two figures. Here, the filter order is inversely related with the resulting time delay, or the phase delay in which the designer should consider as a trade off.

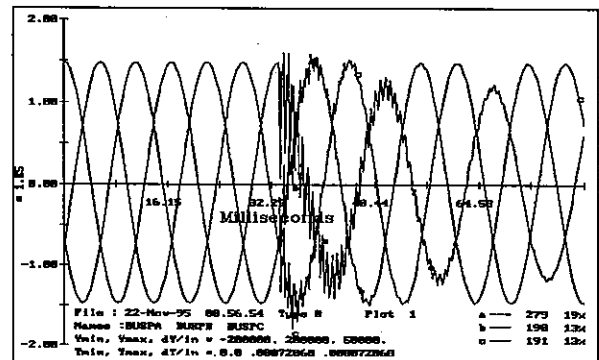


Fig. 2. Voltage waveforms when fault is occurred at fault incidence angle 90°.

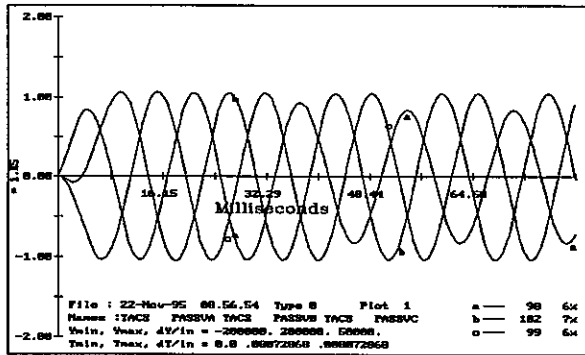


Fig. 3. Voltage waveforms after the anti-aliasing low-pass filtering.

C. Modeling Dc-offset Removal Filter with MODELS

When a fault occurs the measured voltage value consists mostly of the high component while the measured current value are affected by the exponentially decaying dc-offset component. To the contrary the case for the sensed voltage, the measured current value is more affected from the dc-offset at the zero-degree than at the 90-degree. If an algorithm using the normal component, the dc component should be removed for correct result. We rather approached to this problem by using all the three phase components and removed the dc component accordingly.

The current waveforms distorted by the dc component at the fault incidence angle of zero, fault distances of 20% and 70% are shown in Fig. 4 and Fig. 5. Also, the outputs of the dc-offset removal filter is drawn in Fig. 4 and Fig. 5. Note the attenuation of the magnitude and phase delay of the filtered current value in the figure and especially the removal of the dc component at around the zero-degree.

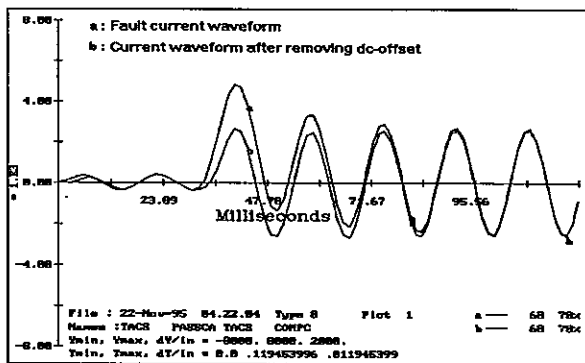


Fig. 4. Current waveforms of fault and after removing dc-offset (fault incidence angle : 0°, fault distance : 20%).

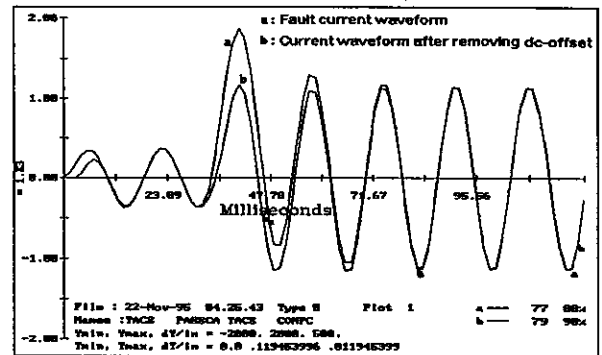


Fig. 5. Current waveforms of fault and after removing dc-offset (fault incidence angle : 0°, fault distance : 70%).

The fore-mentioned anti-aliasing low-pass and the dc-offset removal filtering are implemented in a single EMTP file and the simplicity of controlling the power systems and the protective digital relaying systems is hence ensured.

D. Results

As preprocessing stages the anti-aliasing low-pass filtering and the dc-offset removal filtering are carried to produce the voltage and the current values for the extraction of the fundamental frequency component, which in turn used for the impedance calculation. In order to confirm the impedance convergence, we experimented for each fault types and distances. The system diagram for the simulation is shown in the Fig. 6.

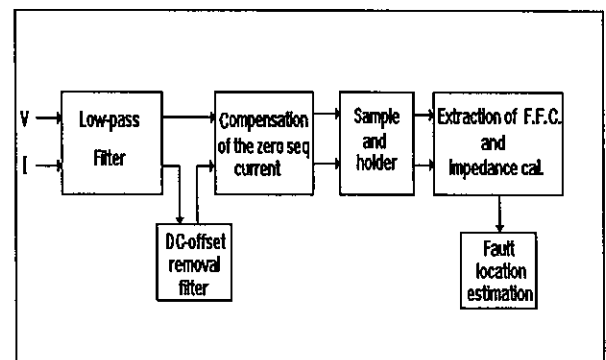
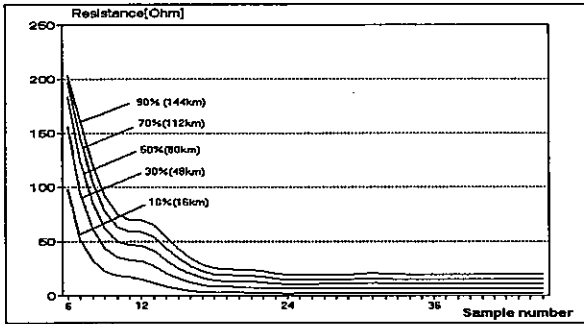
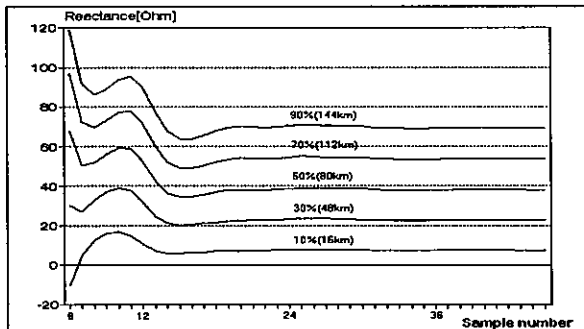


Fig. 6. Overall block diagram of simulation using MODELS.

Fig. 7 shows the convergences of the resistance and the reactance of the fault phase of zero for different fault distances from the half cycles after the fault instance.



(a) resistance



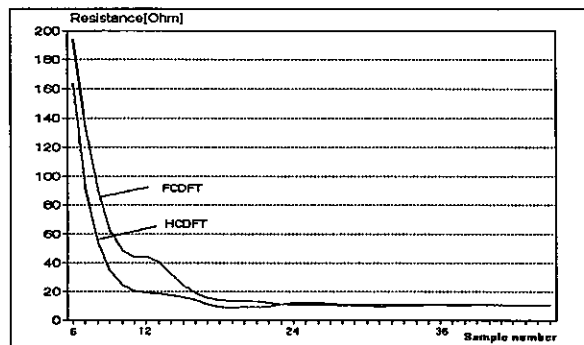
(b) reactance

Fig. 7. Convergence characteristics of impedance at fault incidence angle 0° (using FCDFT).

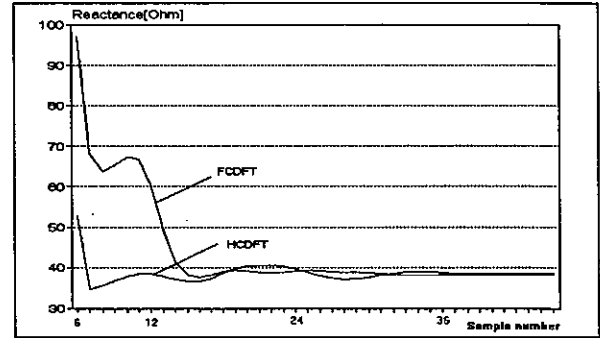
In addition, the full cycle DFT (FCDFT) and the half cycle DFT (HCDFT) are used for the fundamental frequency component extraction. The corresponding results of the resistance and the reactance convergence properties are demonstrated in Fig. 8 for the fault-degree of 45 and the fault distance 50%.

FCDFT and HCDFT each hold its merits in the fundamental frequency component extraction process. More data are used in FCDFT, thus operate slowly while in HCDFT faster computation worths it. The FCDFT trades its speed with the convergence behavior.

In order to show the stability in the impedance convergence the traces are shown in Fig. 9 for FCDFT.



(a) resistance

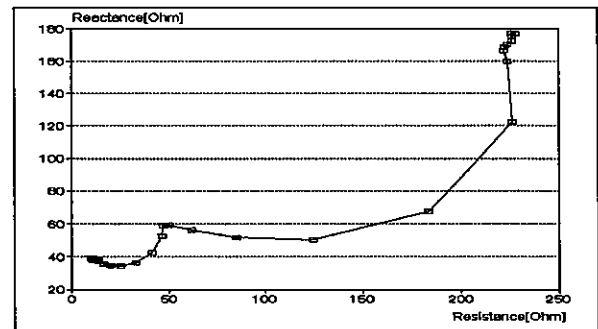


(b) reactance

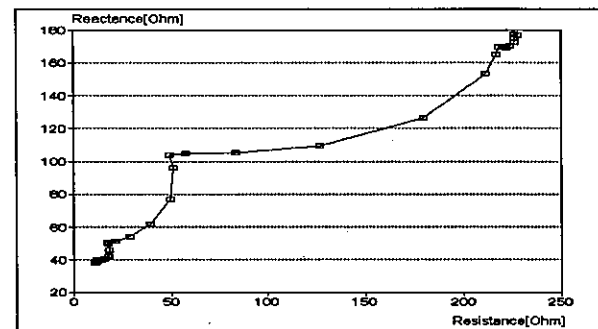
Fig. 8. Comparison of impedance convergence characteristics the FCDFT and HCDFT.

The horizontal axis and the vertical axis are for the reactance and the resistance in the figures. Thus, the resistance converges from the right side and the reactance from the left side. Predefined convergence areas are set for the resistance and the reactance. Protecting trip signals are issued as soon as the convergence reaches the predefined region for further necessary actions to separate the fault segment from the normal operated portion of transmission system.

The fault instances for both simulations are of 50% distance. The fault degrees of 0 and 90 are shown in each figure of the FCDFT's.



(a) fault incidence angle 0°



(b) fault incidence angle 90°

Fig. 9. Impedance trajectory using FCDFT (fault distance : 50%).

V. CONCLUSIONS

Using EMTP MODELS, we presented a simulation algorithm for power systems and digital protective relaying algorithms in a single file format, which helps to reduce the tasks in modeling and accompanying modeling algorithms. It is evident that the electric system and the protective systems should not be considered separately when a digital protective relaying algorithm is to be considered and is hence important to consider the modelings simultaneously.

We conclude from the experiment obtained for this study that the concurrent modeling of the power system and the protective relaying system for transmission lines is desired for more efficient relaying algorithm development and improvement as well as for the evaluation of the existing algorithms.

For further research we would like to improve the speed of the computation which may be obtained via using a high level language such as C and interfacing with EMTP routines.

VI. REFERENCES

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